

Applicant: Takashi Saida et al.
 Serial No.: 10/587,446
 Filing Date: July 25, 2006
 For: PLANAR LIGHTWAVE CIRCUIT, DESIGN METHOD FOR WAVE PROPAGATION
 CIRCUIT, AND COMPUTER PROGRAM

Confirmation No.: 5371
 Att'y Docket No.: 14321.89
 Art Unit: 2874

INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANTU.S. Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>
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Foreign Patent Documents

<u>Examiner Initial*</u>	<u>Document Number</u>	<u>Publication Date</u>	<u>Country or Patent Office</u>	<u>Translation</u>
____ 1	62-017708	01/26/1987	Japan	Partial
____ 2	02-126205	05/15/1990	Japan	Partial
____ 3	05-060929	03/12/1993	Japan	No
____ 4	09-297228	11/18/1997	Japan	No
____ 5	10-090537	04/10/1998	Japan	Partial
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____ 7	2002-90561	03/27/2002	Japan	No

Other Documents

(including author, title, pertinent pages, etc.)

Examiner Initial*

- ____ 8 Z. Weissman et al., *Analysis of Periodically Segmented Waveguide Mode Expanders*, Journal of Lightwave Technology, Vol. 13, No. 10, October 1995, pp. 2053-2058.
- ____ 9 Michael M. Spuhler et al., *A Very Short Planar Silica Spot-Size Converter Using a Nonperiodic Segmented Waveguide*, Journal of Lightwave Technology, Vol. 16, No. 9, September 1998, pp. 1680-1685.

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- 10 Katsunari Okamoto, *Fundamentals of Optical Waveguides*, Academic Press, 2000, pp. 294-303.
- 11 B. Plaun et al., *Optimization of Waveguide Bends and Bent Mode Converters Using a Genetic Algorithm*, 25th International Conference on Infrared and Millimeter Waves (IRMMW2000), September 12-15, 2000.

References Cited by Applicants

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The reference designations "A1," "A2," etc. (referring to Applicant's reference 1, Applicant's reference 2, etc.) will be used by the Examiner in the same manner as Examiner's reference designations "A," "B," "C," etc. on Office Action Form PTO-1142.

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